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REMARKS/ARGUMENTS

Claims 12-20 are pending in this application. By this Amendment, Applicants amend Claim 12.

Claims 12-20 were rejected under the judicially created doctrine of obviousness-type double patenting over Claims 2-10 of U.S. Patent No. 6,678,145.

Claim 12 has been amended to recite:

A wiring connection structure of a decoupling capacitor to be connected to a power supply circuit for a MPU chip provided in a microprocessing unit, the decoupling capacitor comprising:

a capacitor body having first and second major surfaces opposed to each other, the capacitor body including a laminated stack of a plurality of dielectric layers, at least a pair of first and second internal electrodes opposed to each other with at least one of the dielectric layers being disposed therebetween;

a plurality of first feedthrough conductors perforating through at least one of the dielectric layers provided within the capacitor body, the first feedthrough conductors being electrically insulated from the second internal electrodes and electrically connected to the first internal electrodes;

a plurality of second feedthrough conductors perforating through the capacitor body and provided within the capacitor body, the second feedthrough conductors are electrically insulated from the first internal electrodes and are electrically connected to the second internal electrodes, the first and second feedthrough conductors are arranged to offset the magnetic fields induced by the electric current flowing through the internal electrodes;

at least one of power supply lines and signal lines connected to the MPU chip are grounded to a mother board via at least one of the feedthrough conductors;

a plurality of first external terminal electrodes arranged so as to correspond to the respective first feedthrough conductors and electrically connected to respective ones of the first feedthrough conductors; and

a plurality of second external terminal electrodes, which are arranged to correspond to respective ones of the second feedthrough conductors and electrically connected to respective ones of the second feedthrough conductors; wherein

the first external terminal electrodes are located at least on the first major surface of the capacitor body and extend substantially parallel to the internal electrodes, and the second external terminal electrodes are

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located on both the first major surface and the second major surface in
opposed relation to the first major surface; and

**a cross sectional area of the feedthrough conductors
connected to the power supply lines is greater than the remaining
feedthrough conductors.** (emphasis added)

Support for the amendment to Claim 12 is found, for example, in the paragraph
bridging pages 29 and 30 and the paragraph bridging pages 33 and 34 of the originally
filed specification.

The Examiner alleged that Claim 2 (including all of the limitations of Claim 1) of
U.S. Patent No. 6,678,145 includes all of the elements claimed in Claim 12 of the
present application. However, neither Claim 2 nor any other claim of U.S. Patent No.
6,678,145 recites the feature of “a cross sectional area of the feedthrough conductors
connected to the power supply lines is greater than the remaining feedthrough
conductors” as recited in Applicants’ Claim 12, as amended herein.

Accordingly, Applicants respectfully request reconsideration and withdrawal of
the double patenting rejection of Claim 12 over Claim 2 of U.S. Patent No. 6,678,145.

In view of the foregoing amendments and remarks, Applicants respectfully submit
that claim 12 is allowable. Claims 13-20 depend upon Claim 12, and are therefore
allowable for at least the reasons that Claim 12 is allowable.

In view of the foregoing amendments and remarks, Applicants respectfully submit
that this application is in condition for allowance. Favorable consideration and prompt
allowance are solicited.

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The Commissioner is authorized to charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 50-1353.

Respectfully submitted,

Date: October 18, 2006

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